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inverter means connected with the DC terminals and operative to provide an inverter voltage at a pair of inverter terminals; the inverter voltage having a fundamental period; the inverter means having a first periodically conducting transistor operative to conduct in its forward direction only during a first conduction period; the first conduction period having a duration substantially shorter than half the duration of the fundamental period; and

load means connected with the inverter terminals and operative to draw a load current therefrom; the load means including energy-storing inductor means and a gas discharge lamp.

In claim 33, line 23, the term "inpyt" is deleted and substituted with the word --input--.

REMARKS

On basis of conversation with Examiner, claim 13 should now be allowable over the cited art; which therefore should place the application as a whole in condition for allowance.

During this conversation, Examiner suggested that -- at the time of the invention -- power supplies providing a DC voltage of magnitude higher than the peak magnitude of the power line voltage were well known. Applicant agreed that such was indeed the case, but that it was neither well known nor usual practice to use such a power supply in an inverter-type ballast.


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